	Туре	Hits	Search Text	DBs	Time Stamp	Comments
Н	BRS	29390	test\$3 with (PCB or (printed adi circuit adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:25	
8	BRS	L	test\$3 with (PCB or (printed adi circuit adj board)) and substrate\$1 with optical with transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
m	BRS	6198	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:25	
7	BRS	1678	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
വ	BRS	353	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
9	BRS	249	test\$3 with (PCB or (printed adi circuit adi board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
7	BRS	.8	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:43	
&	BRS	7	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (subcomponent\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:28	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
თ	BRS	7	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
10	BRS	. 20	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
11	BRS	15	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:45	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
12	BRS	0	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (subcomponent\$1) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:50	
13	BRS	4	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 with component\$1 or (subcomponent\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:06	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	22	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:43	
15	BRS	20	1 (PCB or li circuit adj l electric\$4 ct\$4 and vith base with d (transceiver\$1 ce or coupon) b1 and ent\$1 or (sub- l) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:12	·

np Comments		10
Time Stamp	2005/10/25	2005/10/25
DBs	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
Search Text	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon) with board\$1 and (subcomponent\$1) or component\$1) or	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) or
Hits		5 6
Туре	BRS	BRS
	10	17

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
æ	BRS	16	or cuit adj cric\$4 nd ase with sceiver\$1 receiv\$4 coupon) or (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:14	
o o	BRS	133	PCB or circuit adjusteric\$4 4 and h base with transceiver\$1 or coupon) and \$1 or (subor) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:18	·

Туре	Hits	Search Text	DBs	Time Stamp	Comments
BRS	1	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	
BRS	1.1	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
22	BRS		test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (subcomponent\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:36	
23	BRS	H 1	(PCB or i circuit\$1 adj nd electric\$4 t\$4 and ith base\$1 with t\$4 with and t\$4 with and nt\$1 or (sub-) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	·

	Type	Hits	Search Text	DBs	Time Stamp	Comments
24	BRS	25	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1) or component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:38	
25	BRS	745	702/117.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
26	BRS	521	209/571,573.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
27	BRS	1234	324/539,755.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
28	BRS	206	710/64.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:47	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	9399	US-PGPUB; 324/539,754,755,757,758,76 USPAT; EPO; 5.ccls. IBM TDB	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:40	
0	BRS	9	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with component\$1) and (remov\$4 or insert\$4 or attach\$3) with socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	

Туре	Hits	Search Text	DBs	Time Stamp	Comments
BRS	10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:48	
BRS	10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) or and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
33	BRS	10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1) or component\$1) or component\$1) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:00	
34	BRS	10	j.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	·

	Type	Hits	Search Text	DBs	Time Stamp	Comments
35	BRS	17	t\$1 adj ric\$4 \$1 same eiver\$1 (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	·
36	BRS		circuit\$1 adj electric\$4 4 and h base\$1 same transceiver\$1 4 with d \$1 or (sub- or and base with attach\$4 or h component\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	·

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	Туре	Hits	Search Text	DBs	Time Stamp	Comments
37	BRS	17	t\$1 adj ric\$4 \$1 same eiver\$1 (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	
38	BRS	12	th (PCB or adi circuit\$1 adj and electric\$4 ect\$4 and with base\$1 same nd (transceiver\$1 mit\$4 with) and nent\$1 or (sub-\$1) or \$1) or or attach\$4) and or attach\$4) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	

	Туре	Hits	Search Text	DBs	Time Stamp Comments	Comments
6 8	BRS	151	439/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 10:32	
40	BRS	8	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (subcomponent\$1) and electric\$4 with (communication\$1 or interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
41	BRS	7	t\$1 adj ric\$4 \$1 same eiver\$1 (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	
42	BRS	21	(PCB or circuit\$1 adj nd electric\$4 1\$4 and th base\$1 same (transceiver\$1 1\$4 with and nt\$1 or (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	

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Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	21	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1) or component\$1) and component\$1) and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	
BRS	233	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	·
BRS	7 7	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
46	BRS	32	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
47	BRS	31	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
48	BRS	7	th (PCB or and circuit\$1 adj and electric\$4 ect\$4 and er\$1 and and oppos\$3 with computer\$4 and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
49	BRS	T	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

. É'	Туре	Hits	Search Text	DBs	Time Stamp	Comments
BRS	. Ø		computer\$4 same test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:39	
BRS	တ္သ	e	computer\$4 and test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:41	
BRS		146	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:50	
BRS		9	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:56	

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	Type	Hits	Search Text	DBs	Time Stamp	Comments
54	BRS	9	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:56	
55	BRS	262	test\$3 with (PCB or (printed adi circuit\$1 adj US-PGPU board\$1)) and electric\$4 USPAT; with connect\$4 and computer\$4 and secur\$3 and IBM_TDB clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	
56	BRS	97	vith (PCB or d adi circuit\$1 adj) and electric\$4 nnect\$4 and c\$4 and secur\$3 amp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
57	BRS	8	with (PCB or ed adi circuit\$1 adj 1)) and electric\$4 onnect\$4 and er\$4 and secur\$3 lamp\$1 and ceiver\$1 or it\$4 or receiv\$4 or ace or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:05	
58	BRS		(PCB or circuit\$1 adj dd electric\$4 s4 and clamp\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:06	
59	BRS	7	(PCB or i circuit\$1 adj nd electric\$4 t\$4 and clamp\$1 and campter\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
09	BRS	591	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) with electric\$4 with connect\$4 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
. 61	BRS	23	or uit\$1 adj ectric\$4 d	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
62	BRS	12	t\$1 adj tric\$4 t\$1 and	adj US-PGPUB; \$4 USPAT; EPO; JPO; DERWENT; and IBM_TDB	2005/10/25 11:08	
63	BRS	0	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) with electric\$4 with connect\$4 and clamp\$1 with head\$3 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
64	BRS	H	t\$1 adj tric\$4 clamp\$1 m\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
65	BRS	31308	test\$3 with (PCB or (printed adi circuit adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:25	

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Comments

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2005/10/24 17:25

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Туре	Hits	Search Text	DBs	Time Stamp	Comments
BRS	<u>ن</u>	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (subcomponent\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:28	
BRS	Ŋ	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
BRS	6	test\$3 with (PCB or (printed adi circuit adj board)) and substrate\$1 with optical with transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	

L	Type	Hits	Search Text	DBs	Time Stamp Comments	Comments
74	BRS	51	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
75	BRS	. 16	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3 and (transceiver\$1 or interface or coupon) with	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:45	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
92	BRS		test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (subcomponent\$1) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:50	
7.7	BRS	ഗ	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 with component\$1 or (subcomponent\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:06	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
78	BRS		test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:43	
79	BRS	21	li circuit adj le circuit adj lelectric\$4 tt\$4 and lith base with l(transceiver\$1 ce or coupon) sl and snt\$1 or (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:12	

Туре	Hits	Search Text	DBs	Time Stamp	Comments
BRS	09	with (PCB or add add circuit add) and electric\$4 and \$3 with base with smit\$4 or receiv\$4 or receiv\$4 or coupon) and (transceivon) and shopent\$1 or (sub-nt\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	
BRS	5 2 9	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon) with board\$1 and (subcomponent\$1) or component\$1) or component\$1) and upper with surface\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	

ÉÍ .	Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	တ္သ	18	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:14	
BRS	${\mathfrak S}$	15	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon) with board\$1 and (subcomponent\$1) or component\$1) and trace\$1 and expose\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:18	

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	Type	Hits	Search Text	DBs	Time Stamp	Comments
84	BRS	7	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	
82	BRS	13	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:28	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
9 8	BRS	13	or uit\$1 adj ctric\$4 d se with sceiver\$1 th r (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	
87	BRS	13	(PCB or in circuit\$1 adj and electric\$4 t\$4 and ith base\$1 with t\$4 with and t\$4 with and and and nt\$1 or (sub-) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
80	BRS	57 88	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1) or component\$1) or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:38	
68	BRS	11292	702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	·
0	BRS	1223	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	

	Type	Hits	Search Text	DBs	Time Stamp Comments	Comments
	BRS	325	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
92	BRS	4 2	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.) and test\$3 with (PCB or (printed adicircuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	

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10/25/05, EAST Version: 2.0.1.4

	Туре	Hits	Search Text	DBs	Time Stamp	Comments	
e 6	BRS	0	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.) and test\$3 with (PCB or (printed adicircuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (subcomponent\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34		
94	BRS	219	710/64.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:47		

10/25/05, EAST Version: 2.0.1.4

	Type	Hits	Search Text	DBs	Time Stamp	Comments
95	BRS	9	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) or with socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	
96	BRS	11	ircuit\$1 adj electric\$4 and base\$1 same ransceiver\$1 with l or (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:48	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
26	BRS	1 1		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	
8 6	BRS	14	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB	2005/10/25 10:34	·

test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or

	Type	Hits	Search Text	DBs	Time Stamp	Comments
101	BRS	. 20	t\$1 adj ric\$4 \$1 same eiver\$1 (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	
102	BRS	m	circuit\$1 adj electric\$4 4 and h base\$1 same transceiver\$1 4 with d \$1 or (sub- or and base with attach\$4 or h component\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	

10/25/05, EAST Version: 2.0,1.4

	Type	Hits	Search Text	DBs	Time Stamp	Comments
103	103 BRS	50	t\$1 adj ric\$4 \$1 same eiver\$1 (sub-	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	
104	BRS	4 5	th (PCB or adi circuit\$1 adj and electric\$4 ect\$4 and with base\$1 same nd (transceiver\$1 mit\$4 with) and nent\$1 or (sub-\$1) or \$1) or or attach\$4) and or attach\$4) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	

Type	Hits	Search Text	DBs	Time Stamp Comments	Comments
BRS	155	439/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:32	
BRS	11447	702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 IBM_TDB 5.ccls. or 439/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	-
BRS	1225	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 5.ccls. or 439/39.ccls.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	

<u>.</u>	Туре	Hits	Search Text	DBs	Time Stamp	Comments
108 B	BRS	326	(702/117.ccls. or 209/571,573.ccls. or 710/64.ccls. or 324/539,754,755,757,758,76 ts.ccls. or 439/39.ccls.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
109 B	BRS	43	ccls. or 13.ccls. or 1s. or 54,755,757,758,76 r 439/39.ccls.) 3 with (PCB or adi circuit adj nd electric\$4 ection\$1 and adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	·

	Type	Hits	Search Text	DBs	Time Stamp	Comments
.10	BRS	0	or 57,758,76 .ccls.) PCB or lit adj ric\$4 and e and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	
11	BRS	1 5	cls. or or 55,757,758,76 9/39.ccls.) th (PCB or circuit adj lectric\$4 on\$1 and base	US-PGPUB	2005/10/25 10:34	·

	Type	Hits	Search Text	DBs	Time Stamp	Comments
112	BRS	4 0	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with component\$1) or component\$1) or component\$1) or component\$1) and electric\$4 with (communication\$1 or interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	
113	BRS	9 8	h (PCB or di circuit\$1 adj and electric\$4 ct\$4 and with base\$1 same d (transceiver\$1 it\$4 with and ent\$1 or (sub- 1) or 1) and with	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
114	BRS	5.4	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1) or component\$1) or component\$1) and component\$1) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	
115	BRS	5.4	h (PCB or di circuit\$1 adj and electric\$4 ct\$4 and with base\$1 same d (transceiver\$1 it\$4 with and ent\$1 or (sub- 1) or 1) and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
116	BRS	272	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:37	
117	BRS	48	or nit\$1 adj ctric\$4 d oppos\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
118	BRS	35	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
119	BRS	34	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
120	BRS	M	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 US-PGPU with connect\$4 and USPAT; transceiver\$1 and oppos\$3 with IBM_TDB end\$1 and computer\$4 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
121	BRS	വ	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
122	BRS	[-]	computer\$4 same test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:39	
123	BRS	152	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
124	BRS	7	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
125	BRS	7	r it\$1 adj tric\$4 oppos\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
126 BRS		282	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 and clamp\$1	adj US-PGPUB; 54 USPAT; EPO; JPO; DERWENT; and IBM_TDB	2005/10/25 11:02	
127	BRS	102	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and secur\$3 with clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
128	BRS		test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
129	BRS	ო ო	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
130	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
131	BRS	7.7	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25	
132	BRS	2	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
133	BRS	7	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
134	BRS	159	test\$3 with (PCB or (printed adj circuit\$1 adj US-PGPU board\$1)) and electric\$4 USPAT; with connect\$4 and computer\$4 and secur\$3 and IBM_TDB clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
135	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	·
136	BRS	33	with (PCB or ed adj circuit\$1 adj l) and electric\$4 onnect\$4 and secur\$3 lamp\$1 and seiver\$1 or t\$4 or receiv\$4 or teceiv\$4 or teceiv\$4 or	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:05	·
137	BRS	H	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 with head\$2 and computer\$4 with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:06	·

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
138	BRS	7	with (PCB or ed adj circuit\$1 adj 1)) and electric\$4 onnect\$4 and clamp\$1 ead\$2 and computer\$4 rt\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
139	BRS	333	h (PCB or dj circuit\$1 adj with electric\$4 ct\$4 and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	,
140	BRS		h (PCB or dj circuit\$1 adj with electric\$4 ct\$4 and with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
141	BRS	7	(PCB or ij circuit\$1 adjrith electric\$4 t\$4 and with port\$1 and	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:08	
142	BRS	1	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) with electric\$4 with connect\$4 and clamp\$1 with head\$3 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	

	Туре	Hits	Search Text	DBs	Time Stamp	Comments
143	BRS	1	(PCB or j circuit\$1 adj ith electric\$4 t\$4 and clamp\$1 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
144	BRS	2	or uit\$1 adj ectric\$4 id clamp\$1 arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:42	
145	BRS	0	mills-randell.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:22	
146	BRS	6957	mills.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:22	
147	BRS	47	mills.in. and mills near randell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM IDB	2005/10/25 12:22	
148	BRS	34	mills.in. and mills near randell and hydro\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 12:23	

	Type	Hits	Search Text	DBs	Time Stamp Comments	Comments
149 BRS	BRS	4	mills.in. and mills near randell and hydro\$5 and maxwellian	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 12:23	

	D	Н	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Retrieva l Classif
1			US 2005005219 3 A1	20050310	ω	Notched electrical test probe tip	324/754		324/754
7	×		US 2005000164 0 A1	20050106	19	Apparatus for scan testing printed circuit boards	324/754		324/754
т	×		US 2004020489 3 A1	20041014	25	Instruction register and access port gated clock for scan cells	702/117		702/117
4	×		US 2004019605 8 A1	20041007	14	Method for fabricating a probe pin for testing electrical characteristics of an apparatus	324/754		324/754
വ	×	×	US 2004009213 5 A1	20040513	20	Electronic assembly tester and method for optoelectronic device	439/39		439/39
9	×		us 2004002203 9 A1	20040205	9	Reduced inductance contactor	361/767	174/265; 324/754; 324/765; 361/769	324/754; 324/765
7	×		US 2003018943 8 A1	20031009	&	ELECTRICAL TEST PROBE WEDGE TIP	324/754	,	324/754

Inventor	S	C	പ്പ	N	n	4	5		Image Doc. Displayed	ЪТ
Campbell, Julie A.	×							us 2	20050052193	
Swart, Mark A.	·							ns 2	20050001640	
Whetsel, Lee D. JR.								ns 2	20040204893	
Matsunaga, Noriaki et al.								ns 2	20040196058	
Hofmeister, Rudolf J. et al.		-	×					ns 2	20040092135	× ×
Ortiz, Alfred E. et al.								sn	20040022039	
Campbell, Julie A. et al.								us 2	20030189438	

	D	1 Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Retrieva l Classif
8	× .	US 2003018943 7 A1	20031009	8	Notched electrical test probe tip	324/754		324/754
6	×	US 2003009496 1 A1	20030522	20	Apparatus for scan testing printed circuit boards	324/754		324/754
10	×	US 2002018047 20021205 1 A1		13	Signal launch connecting techniques	324/758		324/758
11	×	US 2002012590 9 A1	20020912	18	Testing of BGA and other CSP packages using probing techniques	324/765	324/754	324/754; 324/765
12	×	US 2002012590 8 A1	20020912	17	Testing of BGA and other CSP packages using probing techniques	324/765	·	324/765
13	×	US 2002010951.2 9 A1	20020815	08	Planar subassembly for testing IC chips having faces with pressed electrical contacts that carry all power and signals for the chips	324/765		324/765

	Inventor	ß	υ	Д	N	т	4	Ŋ.	Image Doc. Displayed	PT
	Campbell, Julie A.							,	US 20030189437	7
	Swart, Mark A.							,	US 20030094961	
<u> </u>	Cartier, Marc								US 20020180471	H
=	Mehta, Rajiv et al.								US 20020125909	o
8	Mehta, Rajiv et al.								us 20020125908	. 8
[3	Tustaniwskyj, Jerry Ihor et al.								us 20020109519	<u></u>

	Þ	1 D	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Retrieva 1 Classif
14	×	US 20() 02009336 A1	US 2002009336 20020718 21 0 A1		Integrate circuit device	324/765		324/765
15	×	US 20(3 A	S 002004398 A1	US 2002004398 20020418 14 3 A1		Chip-testing socket using surface mount techinology	324/755		324/755

	Inventor	ß	υ	<u> </u>	0	М	4	വ	Image Doc. Displayed	PT
	Nagata, Junichi								09886002007 SD	
S	Cheng, Wei-Jen								US 20020043983	